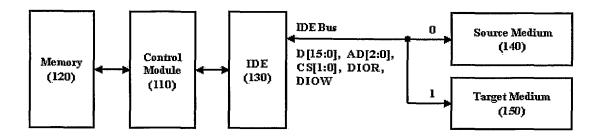
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FIG. 1



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FIG. 2

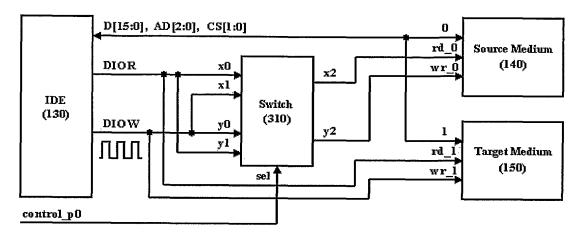
IDE Function Allocation Address Table (200)

Address					Functions	
C S0-	CSI-	DA2	DAI	DAO	Read (DIOR-)	Write (DIOW-)
N	N	x	X.	x	Data bus high impedance	Not used
					Control block registers	
N	A	0	X	x	Data bus kigh impedance	Not used
N	A	I	Ø	x	Data bus high impedance	Not used
N	A	1	1	0	Alternate Status	Device Control
N	A	1	1	1	* * *	Not used
					Command block registers	
A	N	0	0	0	Data	Data
A	И	0	0	1	Eiror	Features
A	N	0	1	0	Sector Count	Sector Count
A	N	Ø	1	1	Sector Number	Sector Number
A	N	1	0	0	Cylinder Low	Cylinder Low
A	N	1	0	1	Cylinder High	Cylinder High
A	И	1	1	0	Device/Head	Device/Head
A	И	1	1	ļ	Status	Command
A	A	×	x	x	Invalid address	Invalid address

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FIG. 3a

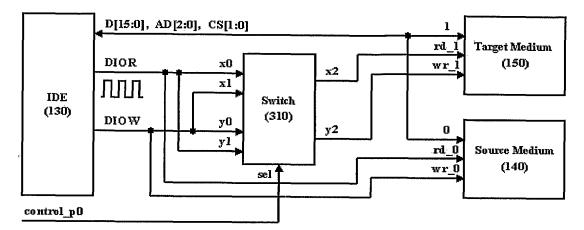
[SEL = I, WRITE Operation]



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FIG. 3b

[SEL = 1, READ Operation]



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FIG. 4

